

## **PALM INTRANET**

Day: Wednesday Date: 6/1/2005 Time: 07:23:26

## **Inventor Name Search Result**

Your Search was:

Last Name = ADKISSON First Name = RICHARD

	Application#	Patent#	Status	Date Filed	Title	Inventor Name 50
P	60576764	Not Issued			PERFORMANCE MONITORING SYSTEM	ADKISSON, RICHARD W.
	60576763	Not Issued	020	06/03/2004	MATCH CIRCUIT FOR PERFORMING PATTERN RECOGNITION IN A PERFORMANCE COUNTER	ADKISSON, RICHARD W.
	60576646	Not Issued	020	06/03/2004	DURATIN MINIMUM AND MAXIMUM CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W
	60576554	Not Issued	020	06/03/2004	EDGE DETECT CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
	60469504	Not Issued	159	05/10/2003	SYSTEM AND METHOD FOR EFFECTUATING THE TRANSFER OF DATA BLOCKS INCLUDING A HEADER BLOCK ACROSS A CLOCK BOUNDARY	ADKISSON, RICHARD W.
	60469503	Not Issued	159	05/10/2003	SYSTEM AND METHOD FOR EFFECTUATING THE TRANSFER OF DATA BLOCKS ACROSS A CLOCK BOUNDARY	ADKISSON, RICHARD W.
	60469180	Not Issued	159	05/09/2003	GENERAL PURPOSE COUNTERS FOR PERFORMANCE, DEBUG AND COVERAGE	ADKISSON, RICHARD W.
	60469120	Not Issued	159	05/09/2003	PROGRAMMABLE CLOCK SYNCHRONIZER AND CONTROLLER ARRANGEMENT THEREFOR	ADKISSON, RICHARD W.
	11034152	Not Issued	020		PHASE DETECTOR FOR A PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
	11022079	Not Issued	020	12/23/2004	PERFORMANCE MONITORING SYSTEM	ADKISSON, RICHARD W.
	11022023	Not Issued	020	12/23/2004	MATCH CIRCUIT FOR PERFORMING PATTERN RECOGNITION IN A PERFORMANCE COUNTER	ADKISSON, RICHARD W.
	11022021	Not	020	12/23/2004	EDGE DETECT CIRCUIT FOR	ADKISSON,

	Issued			PERFORMANCE COUNTER	RICHARD W.
11021259	Not Issued	020	12/23/2004	DURATION MINIMUM AND MAXIMUM CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10945056	Not Issued	030			ADKISSON, RICHARD W.
10944524	Not Issued	030	09/17/2004	TIMEOUT ACCELERATION FOR GLOBALLY SHARED MEMORY TRANSACTION TRACKING TABLE	ADKISSON, RICHARD W.
10902341	Not Issued	020	II.	COMMUNICATION AMONG PARTITIONED DEVICES	ADKISSON, RICHARD W.
10901773	Not Issued	030		CONTROLLER FOR CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10901762	Not Issued	030	07/29/2004	CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10898693	Not Issued	030	II I		ADKISSON, RICHARD W.
10898404	Not Issued	030	07/23/2004		ADKISSON, RICHARD W.
10897729	Not Issued	030	07/23/2004	DRIET-TOLERANT SYNC PULSE CIRCUIT IN A SYNC PULSE GENERATOR	ADKISSON, RICHARD W.
10890782	Not Issued	030	07/14/2004		ADKISSON, RICHARD W.
10888898	Not Issued	164	07/08/2004	METHOD AND SYSTEM FOR SENSING IC PACKAGE ORIENTATION IN SOCKETS	ADKISSON, RICHARD W.
10888897	Not Issued	093	07/08/2004	METHOD AND SYSTEM FOR SENSING IC PACKAGE ORIENTATION IN SOCKETS	ADKISSON, RICHARD W.
10756448	Not Issued	020	01/12/2004	CONTROLLING DATA DELIVERY	ADKISSON, RICHARD W.
10756438	Not Issued	030	01/12/2004	DATA FORWARDING V	ADKISSON, RICHARD W.
10635373	Not Issued	020	08/06/2003	MATCH CIRCUIT FOR $$ PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10635372	Not Issued	020		COVERAGE DECODER CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10635371	Not Issued	020		COVERAGE CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10635369	Not Issued	020	08/06/2003	INCREMENT/DECREMENT CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10635103	Not Issued	020		DATA SELECTION CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.

10635083	Not Issued	020	08/06/2003	GENERAL PURPOSE PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10635079	Not Issued	020	08/06/2003	ZEROING CIRCUIT FOR PERFORMANCE COUNTER	ADKISSON, RICHARD W.
10630317	Not Issued	030	07/30/2003	SYSTEM AND METHOD FOR COMPENSATING FOR SKEW BETWEEN A FIRST CLOCK SIGNAL AND A SECOND CLOCK SIGNAL	ADKISSON, RICHARD W.
10630298	6864722	150	II i	PHASE DETECTOR FOR A PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10630297	Not Issued	030	07/30/2003	SYSTEM AND METHOD FOR MAINTAINING A STABLE SYNCHRONIZATION STATE IN A PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10630182	Not Issued	030	07/30/2003	CONTROLLER ARRANGEMENT FOR A PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
<u>10630159</u>	Not Issued	030	07/30/2003	PROGRAMMABLE CLOCK SYNCHRONIZER	ADKISSON, RICHARD W.
10629989	Not Issued	030	07/30/2003	SYSTEM AND METHOD FOR SYNCHRONIZING MULTIPLE SYNCHRONIZER CONTROLLERS	ADKISSON, RICHARD W.
10625365	Not Issued	030	07/23/2003	SYSTEM-AND METHOD FOR EFFECTUATING THE TRANSFER OF DATA BLOCKS ACROSS A CLOCK BOUNDARY	ADKISSON, RICHARD W.
10625291	Not Issued	030	07/23/2003	SYSTEM AND METHOD FOR EFFECTUATING THE TRANSFER OF DATA BLOCKS INCLUDING A HEADER BLOCK ACROSS A CLOCK BOUNDARY	ADKISSON, RICHARD W.
10435127	Not Issued	020	05/09/2003	SYSTEM AND METHOD FOR DETERMINING TRANSACTION TIME-OUT	ADKISSON, RICHARD W.
10434925	Not Issued	030	05/09/2003	SYSTEM AND METHOD FOR ERROR CAPTURE AND LOGGING IN COMPUTER SYSTEMS	ADKISSON, RICHARD W.
10434920	Not Issued	030	05/09/2003	SYSTEMS AND METHODS CONTROLLING TRANSACTION DRAINING FOR ERROR RECOVERY	ADKISSON, RICHARD W.
10434657	Not Issued	041	05/09/2003	SYSTEMS AND METHODS FOR GENERATING TRANSACTION DENTIFIERS	ADKISSON, RICHARD W.
<u>10434656</u>	Not Issued	020	14	SYSTEMS AND METHODS FOR COMBINING A SLOW DATA	ADKISSON, RICHARD W.

				STREAM AND A FAST DATA STREAM INTO A SINGLE FAST DATA STREAM	
10420577	<u>6786760</u>	150		METHOD AND SYSTEM FOR SENSING IC PACKAGE ORIENTATION IN SOCKETS	ADKISSON, RICHARD W.
10397555	Not Issued	030	/	SYSTEM AND METHOD FOR TRANSFERRING DATA FROM A FIRST CLOCK DOMAIN TO A SECOND CLOCK DOMAIN	ADKISSON, RICHARD W.
09887797	Not Issued	071		SYNC PULSE COMPENSATION AND REGENERATION IN A CLOCK SYNCHRONIZER CONTROLLER	ADKISSON, RICHARD W.
<u>09887793</u>	Not Issued	041		SYSTEM AND METHOD FOR SYNCHRONIZING DATA TRANSFER ACROSS A CLOCK DOMAIN BOUNDARY	ADKISSON, RICHARD W.

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Application#	Patent#	Status	Date Filed	Title	Inventor Name 3
09351988	6385676	150		COHERENT ORDERING QUEUE FOR COMPUTER SYSTEM	ADKISSON, RICHARD W.
08258761	5590304	150		CIRCUITS, SYSTEMS AND METHODS FOR PREVENTING QUEUE OVERFLOW IN DATA PROCESSING SYSTEMS	ADKISSON, RICHARD W.
06607984	4523203	150	1	GREY SCALE THERMAL PRINTER CONTROL SYSTEM	ADKISSON, RICHARD W.

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